



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Salvatore Polizzi, et al.

Title of Invention: **NON VOLATILE MEMORY DEVICE ARCHITECTURE,  
FOR INSTANCE A FLASH KIND, HAVING A SERIAL  
COMMUNICATION INTERFACE**

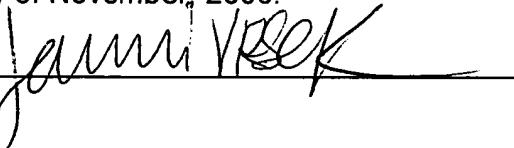
Serial No.: 10/727,341

Filing Date: November 26, 2003

Attorney Dkt. No.: 2110-90-3

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited in the United States Postal Service as First Class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 1<sup>st</sup> day of November, 2006.

Signature 

**COMMENTS ON ALLOWANCE**

November 1, 2006

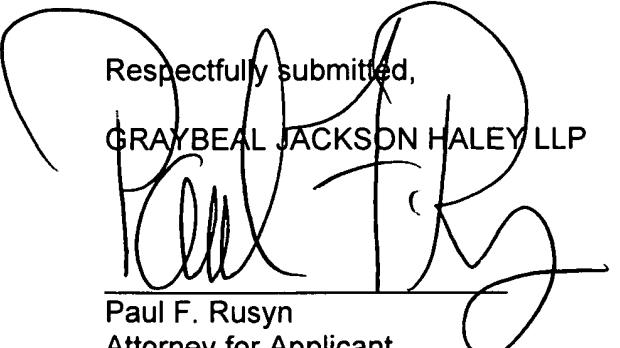
TO THE COMMISSIONER FOR PATENTS:

While the combinations of elements recited in the allowed claims are patentable, the undersigned would like to point out that some or all of these individual elements may be broadened such that the resulting combination is still patentable. Applicants may elect to pursue such claims, or to pursue claims directed to other aspects of the present invention, through a continuation or reissue application, or through a reexamination proceeding as appropriate.

DATED this 1<sup>st</sup> day of November, 2006.

Respectfully submitted,

GRAYBEAL JACKSON HALEY LLP

  
Paul F. Rusyn  
Attorney for Applicant  
Registration No. 42,118  
155-108th Avenue N.E., Ste 350  
Bellevue, WA 98004-5973  
(425) 455-5575